



SUPPLEMENTAL APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Number:: 10/532,105
Application Date:: 10/12/05
Application Type:: **REGULAR**
Subject Matter:: **UTILITY**
CD-ROM or CD-R?:: **NONE**
Title:: **METHOD AND DEVICE FOR
SYNTHESISING AN ELECTRICAL
ARCHITECTURE**
Attorney Docket Number:: 271243US2X PCT
Total Drawing Sheets:: 20

INVENTOR INFORMATION

Applicant Authority Type:: **INVENTOR**
Primary Citizenship Country:: **France**
Status:: **FULL CAPACITY**
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Applicant Authority Type:: **INVENTOR**
Primary Citizenship Country:: **France**
Status:: **FULL CAPACITY**
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CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of	PCT/FR03/03107	10/21/03

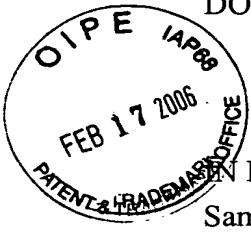
FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
02/13112	France	10/21/02	YES

ASSIGNMENT INFORMATION

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0580



DOCKET NO.: 271243US2XPCT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RE APPLICATION OF:

Samuel BOUTIN, et al.

GROUP: 2876

SERIAL NO: 10/532,105

FILED: October 12, 2005

EXAMINER:

FOR: METHOD AND DEVICE FOR SYNTHESISING AN ELECTRICAL
ARCHITECTURE

SUBMISSION OF SUPPLEMENTAL APPLICATION DATA SHEET

Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the above-identified application for the purpose of correcting the 2nd applicant's name.

Respectfully Submitted,

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